



10/612,831

PATENT

CofC

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. : 7,395,532
Issue Date : July 1, 2008
Inventor : Antonio Maria Borneo et al.

Docket No. : 851763.435
Date : September 3, 2008

Certificate

Mail Stop Certificate of Correction
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SEP 09 2008
of Correction

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents:

A certificate of correction is respectfully requested in the above-identified patent.

The following errors have been made:

In column 14, claim 17, line 19, the section "processor to said instruction set stream" is erroneous and instead should read as -- processor to an instruction set stream --.

In Column 14, claim 20, line 42, the section "load measurement comprises number of operations" is erroneous and instead should read as -- load measurement comprises a number of operations --.

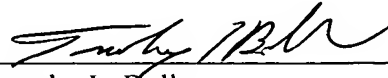
The errors are of a clerical nature, are of minor character, would not constitute new matter or require reexamination, and were made in good faith.

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Attached is the certificate of correction, which indicates the corrections to be made, by reference to the category and page number in the printed patent. The Director is hereby authorized to charge payment of any fees associated with this communication only to Deposit Account No. 19-1090.

Respectfully submitted,
SEED Intellectual Property Law Group PLLC



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Enclosures:

Certificate of Correction
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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO : 7,395,532 B2
DATED : July 1, 2008
INVENTOR : Antonio Maria Borneo et al.

It is certified that an error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

Column 14

Line 19, "processor to said instruction set stream" should read as -- processor to an instruction set stream --

Line 42, "load measurement comprises number of operations" should read as -- load measurement comprises a number of operations --

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